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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant:	Shubhendu S. MUKHERJEE	§		
Serial No.:	Not Yet Assigned	§	Group Art Unit:	Unknown
Filed:	Concurrently Herewith	§	Examiner:	Unknown
For:	Load Value Queue Input Replication In A Simultaneous And Redundantly Threaded Processor	§		

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Attorney Docket No. 1662-37500
Client Ref. No. P00-3162
Date: April 19, 2001

Sir:

This Information Disclosure Statement, including completed Form PTO-1449, comprises a list of pertinent art of which Applicant is aware. A copy of each publication listed on Form PTO-1449 is enclosed herewith.

The submission of this Information Disclosure Statement and the references submitted therewith is not an admission that the art cited is "prior" with respect to the present invention nor is it a representation that no better art exists. Applicant hereby reserves the right to swear behind or otherwise disprove any alleged "prior" nature of any art cited should the facts support and the situation warrant such an action. It is submitted that the art cited does not constitute a bar to the patentability of Applicant's invention under 35 U.S.C. § 102 or § 103.

Applicant believes no fee is due for the filing of these papers. If, however, the Commissioner deems that a fee is due, please charge the fee to Deposit Account 03-2769 of Conley, Rose & Tayon, P.C.

Respectfully submitted,



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